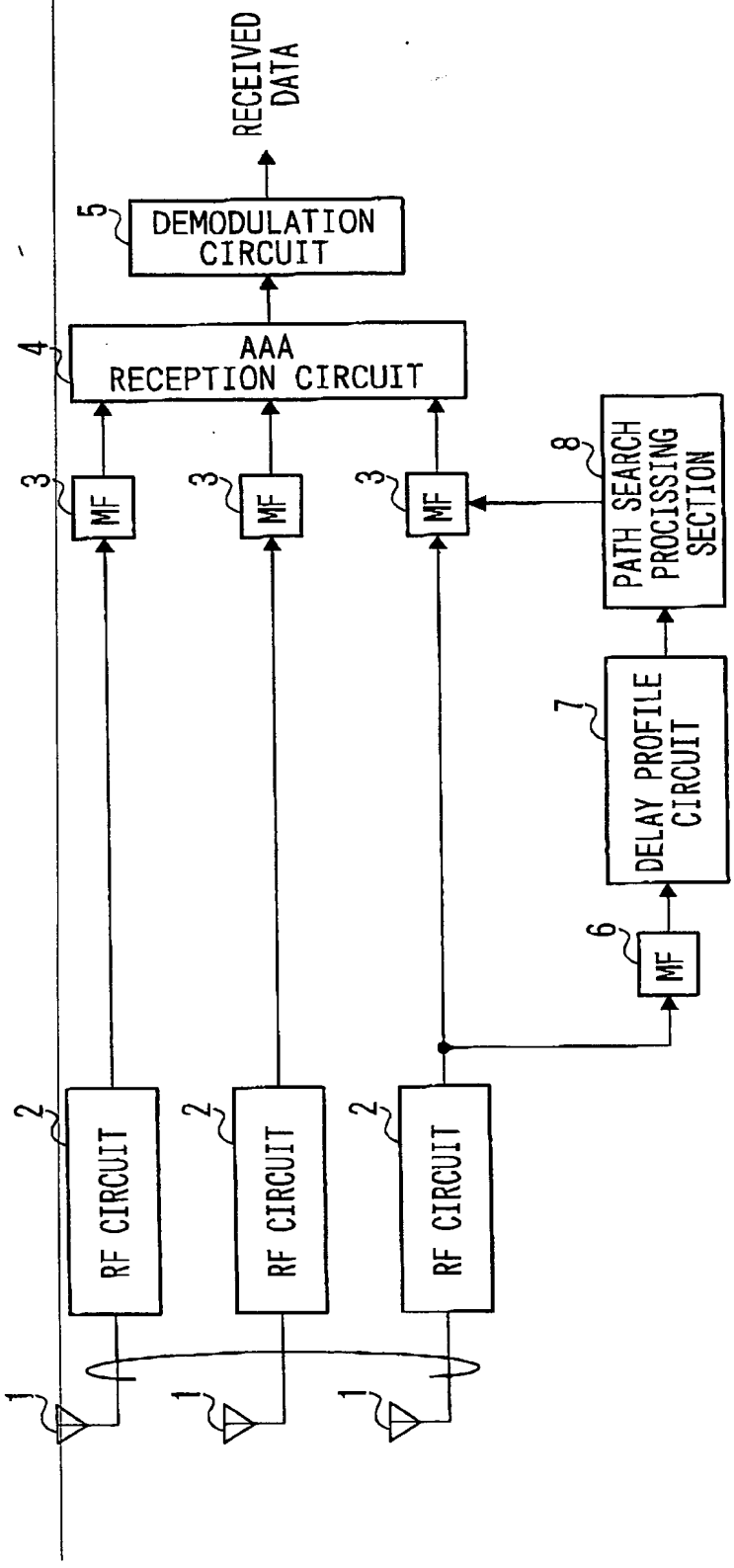


FIG. 1

TO/TS0"62/9E660

EO7E60"627982660

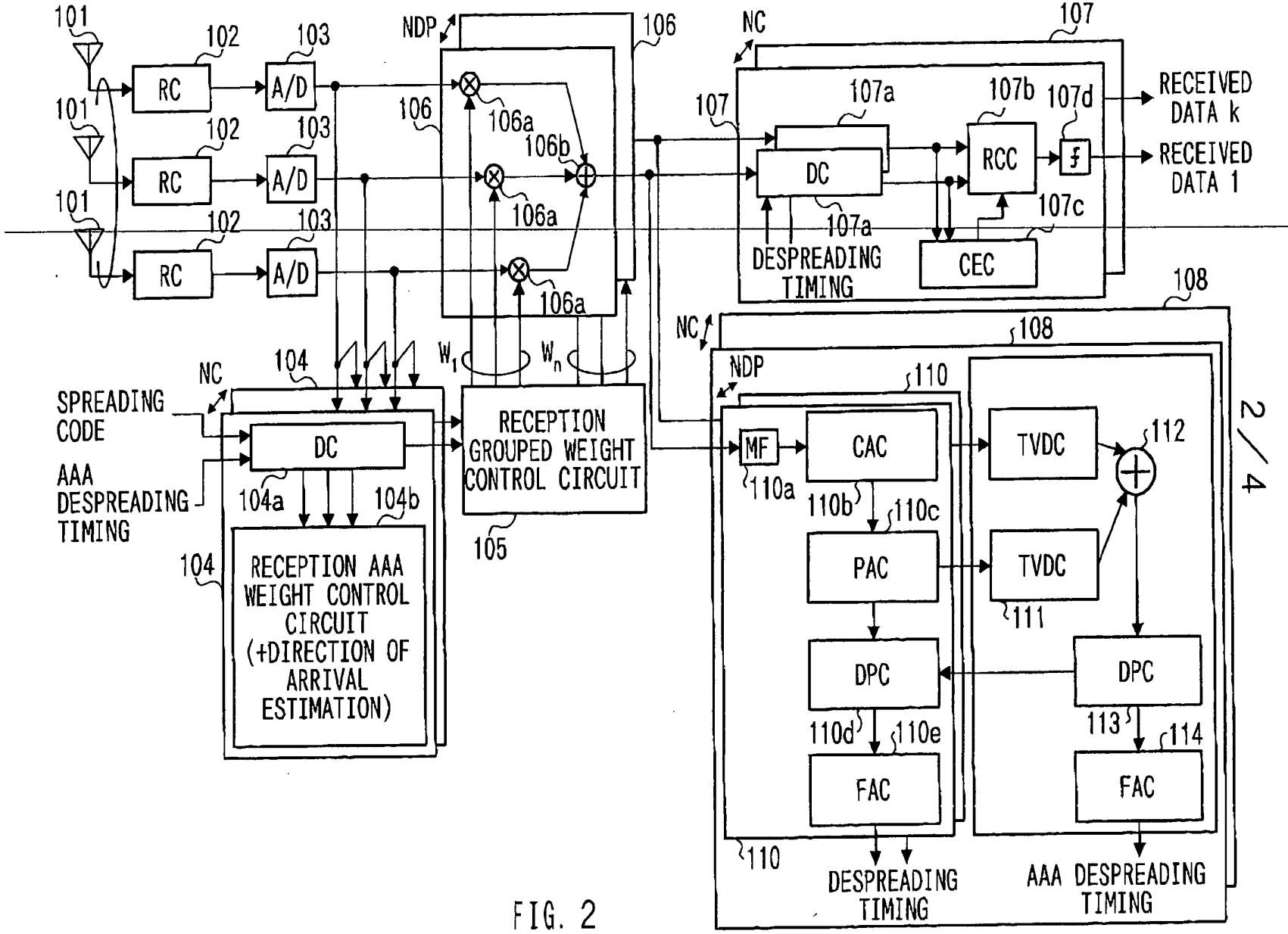
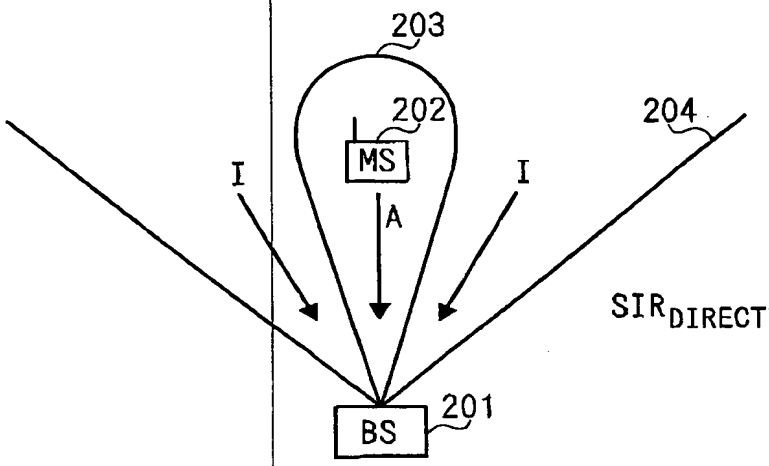


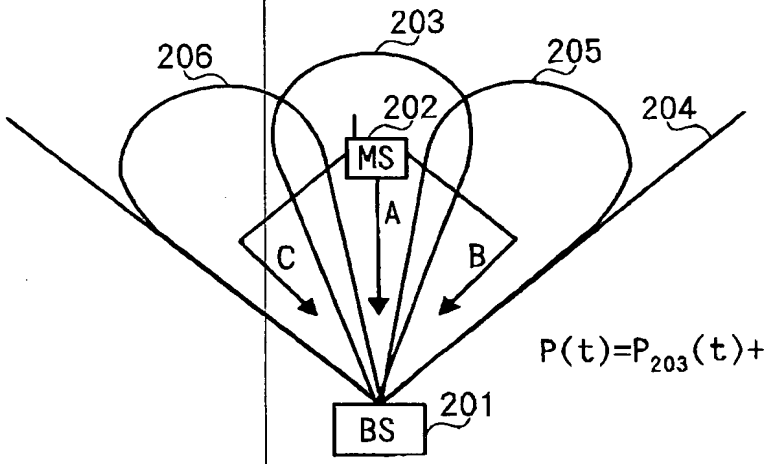
FIG. 2

09/936729



$SIR_{DIRECTIONAL} > SIR_{NONDIRECTIONAL}$

FIG.3



$P(t)=P_{203}(t)+P_{205}(t)+P_{206}(t)$

FIG.4

EDITED: 09/27/03

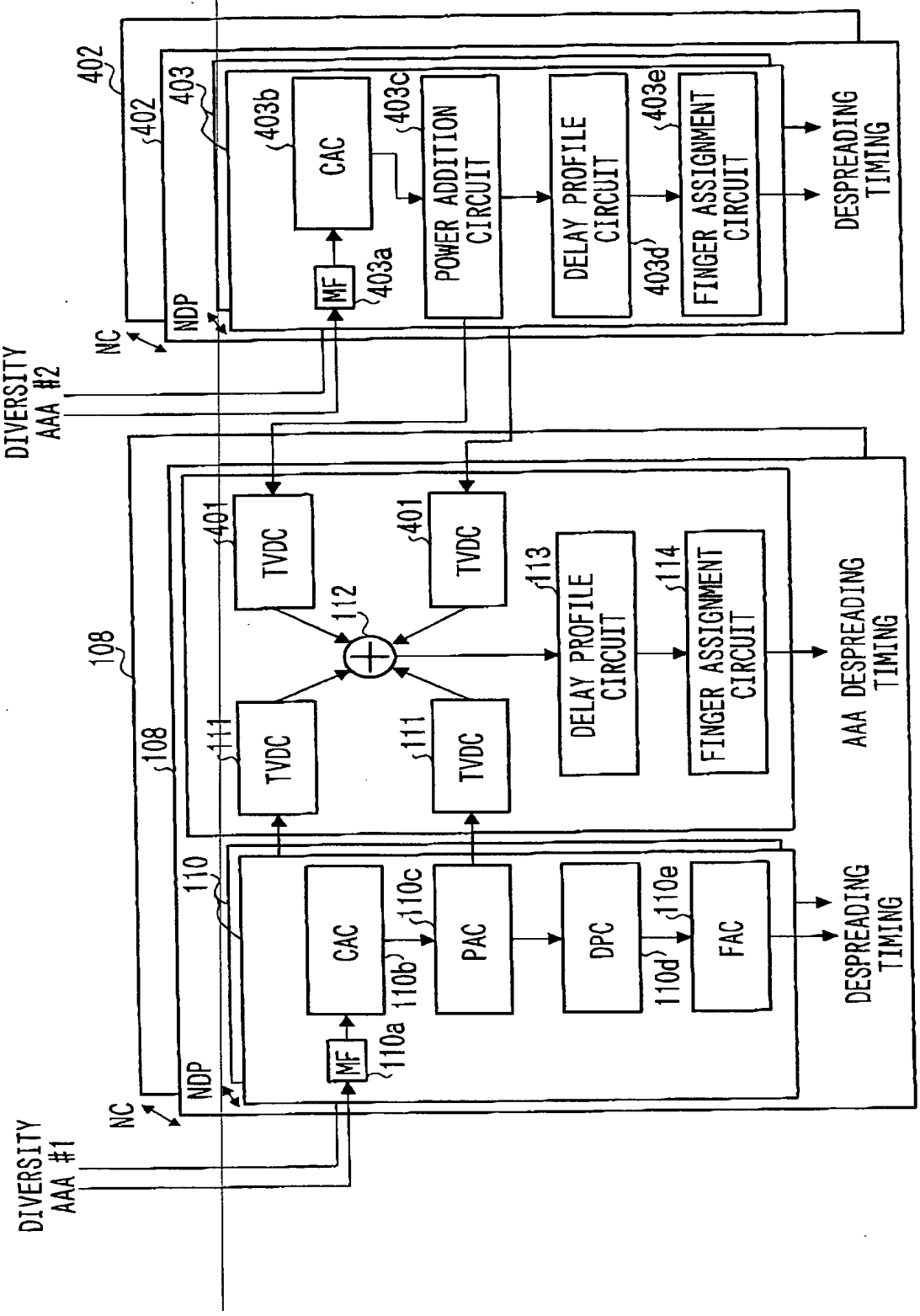

402
402
403
403b
CAC
403c
POWER ADDITION CIRCUIT
DELAY PROFILE CIRCUIT
403e
FINGER ASSIGNMENT CIRCUIT
DESPREADING TIMING
403a
MF
403d
NDP
NC
DIVERSITY AAA #2
108
108
401
TVDC
401
TVDC
112
113
DELAY PROFILE CIRCUIT
114
FINGER ASSIGNMENT CIRCUIT
AAA DESPREADING TIMING
111
TVDC
111
TVDC
110
CAC
110c
PAC
DPC
110e
FAC
DESPREADING TIMING
110a
110b
110d
MF
NDP
NC
DIVERSITY AAA #1

FIG. 5

FIG.2
102 RC : RF CIRCUIT
106,110 NDP : NUMBER OF DIRECTIONAL PATTERNS
107,108 NC : NUMBER OF CHANNELS
104a,107a DC : DESPREADING CIRCUIT
107b RCC : RAKE COMBINING CIRCUIT
107c CEC : CHANNEL ESTIMATION CIRCUIT
110b CAC : COHERENT ADDITION CIRCUIT
110c PAC : POWER ADDITION CIRCUIT
110d,113 DPC : DELAY PROFILE CIRCUIT
110e,114 FAC : FINGER ASSIGNMENT CIRCUIT
111 TVDC : THRESHOLD VALUE DECISION CIRCUIT

FIG.5
108,402 NC : NUMBER OF CHANNELS
110,403 NDP : NUMBER OF DIRECTIONAL PATTERNS
110b,403b CAC : COHERENT ADDITION CIRCUIT
110c PAC : POWER ADDITION CIRCUIT
110d,113 DPC : DELAY PROFILE CIRCUIT
110e,114 FAC : FINGER ASSIGNMENT CIRCUIT
111,401 TVDC : THRESHOLD VALUE DECISION CIRCUIT

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